

ABSTRACT OF THE DISCLOSURE

A path searching circuit is provided which is capable of searching a stable path. A power delay profile calculated by a powering section using an I component correlation value and a Q component correlation value is input to a power level change monitoring section, where a change in a power level is monitored at a sampling point for each of two or more delay profiles to be used for same power adding processing. With correct signal receiving path timing, stable high correlation values are observed, that is, it is observed that the received power level is small in change. On the other hand, with instantaneously high correlation values caused by noises, it is observed that the received power level is great in change. As a result, in the weighing coefficient controlling section, a weighing coefficient for correlation values providing a great change in a received power level is determined so that the received power level becomes low when power is added and so that weighing control is exercised.